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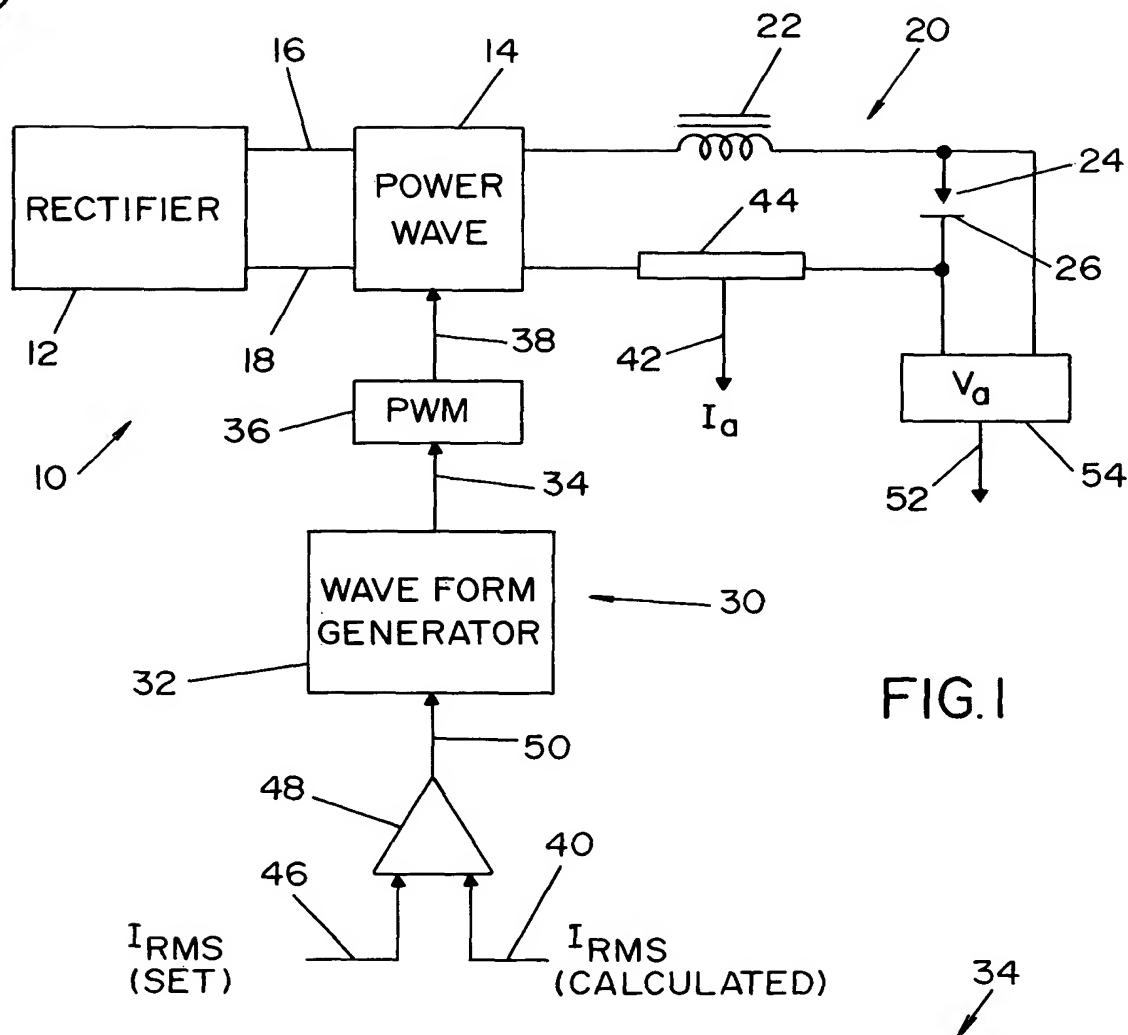


FIG.1

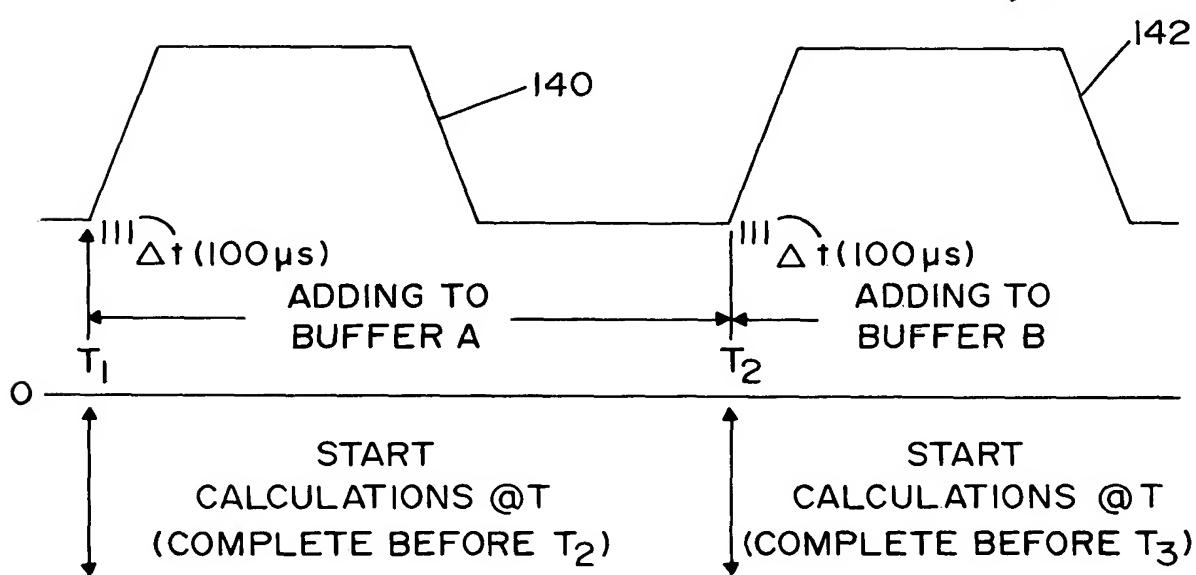


FIG.3A



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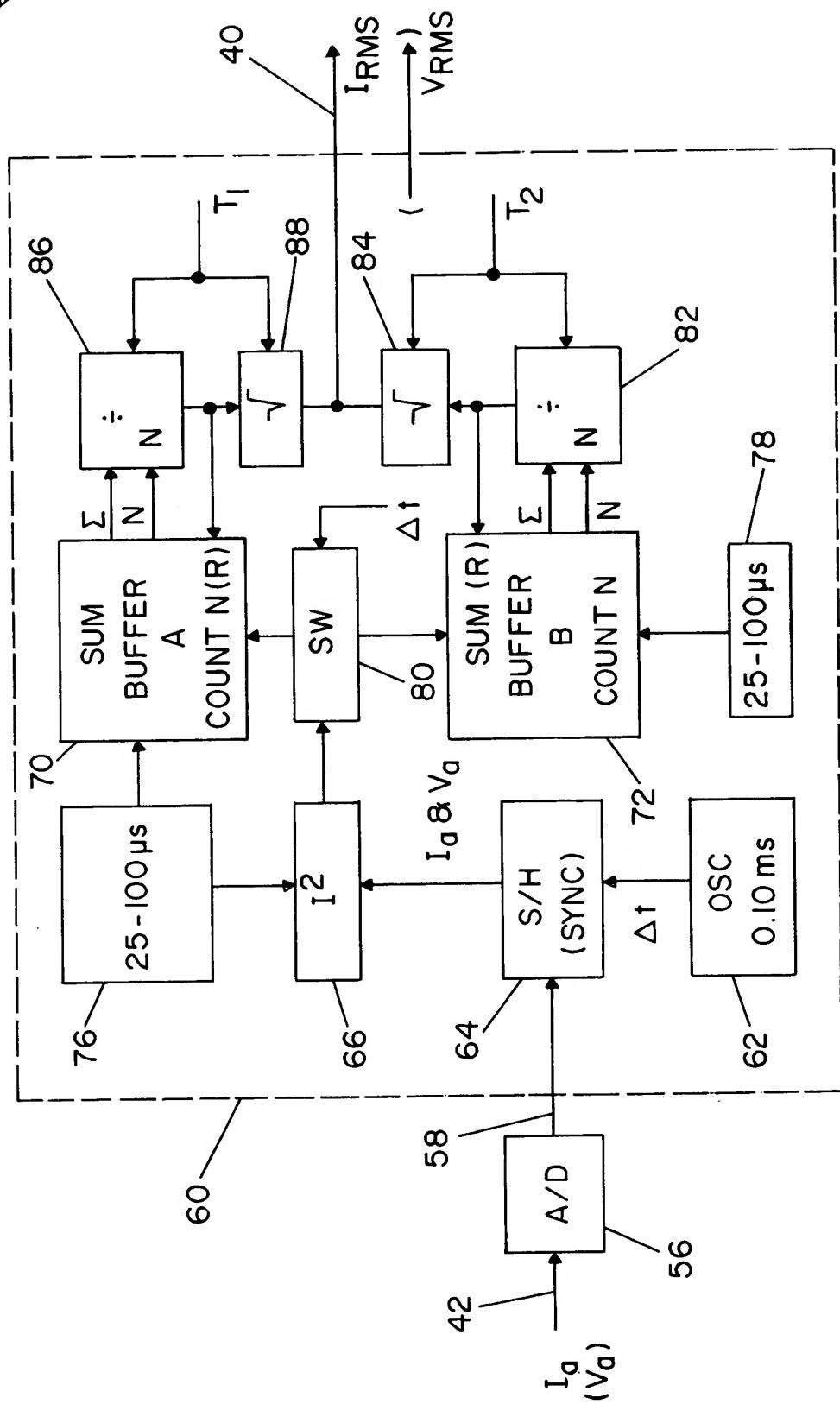


FIG. 2

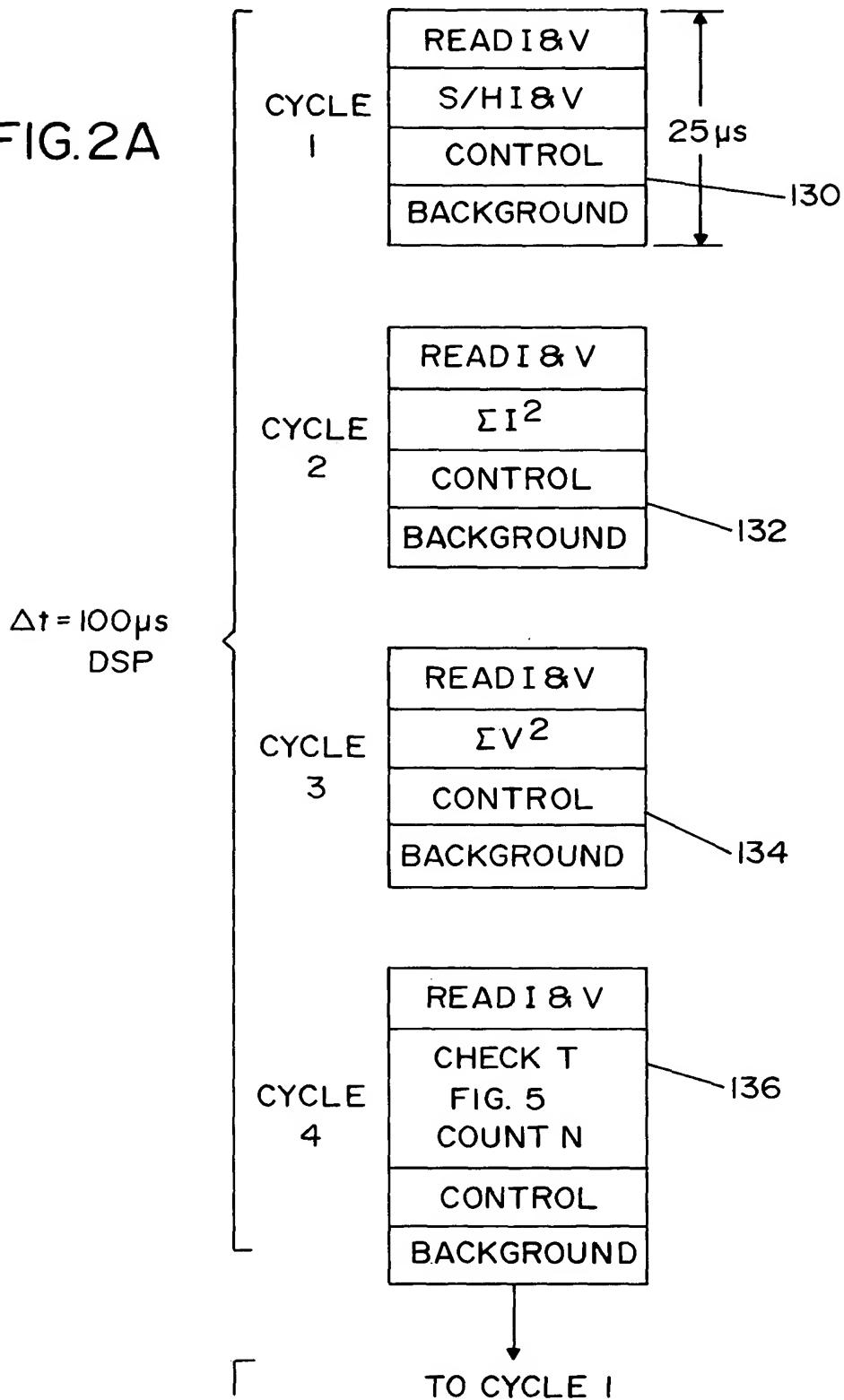
(DSP)



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FIG. 2A



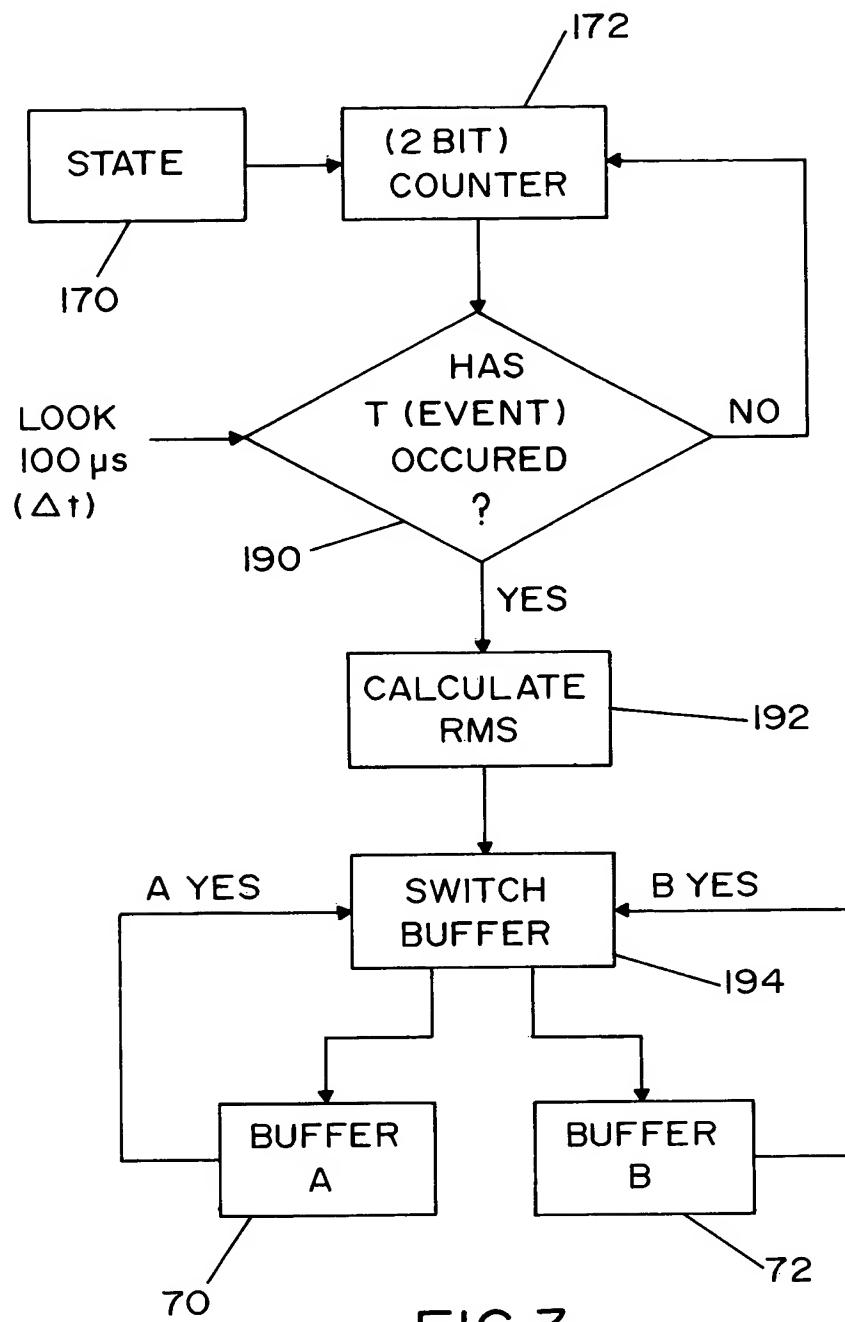
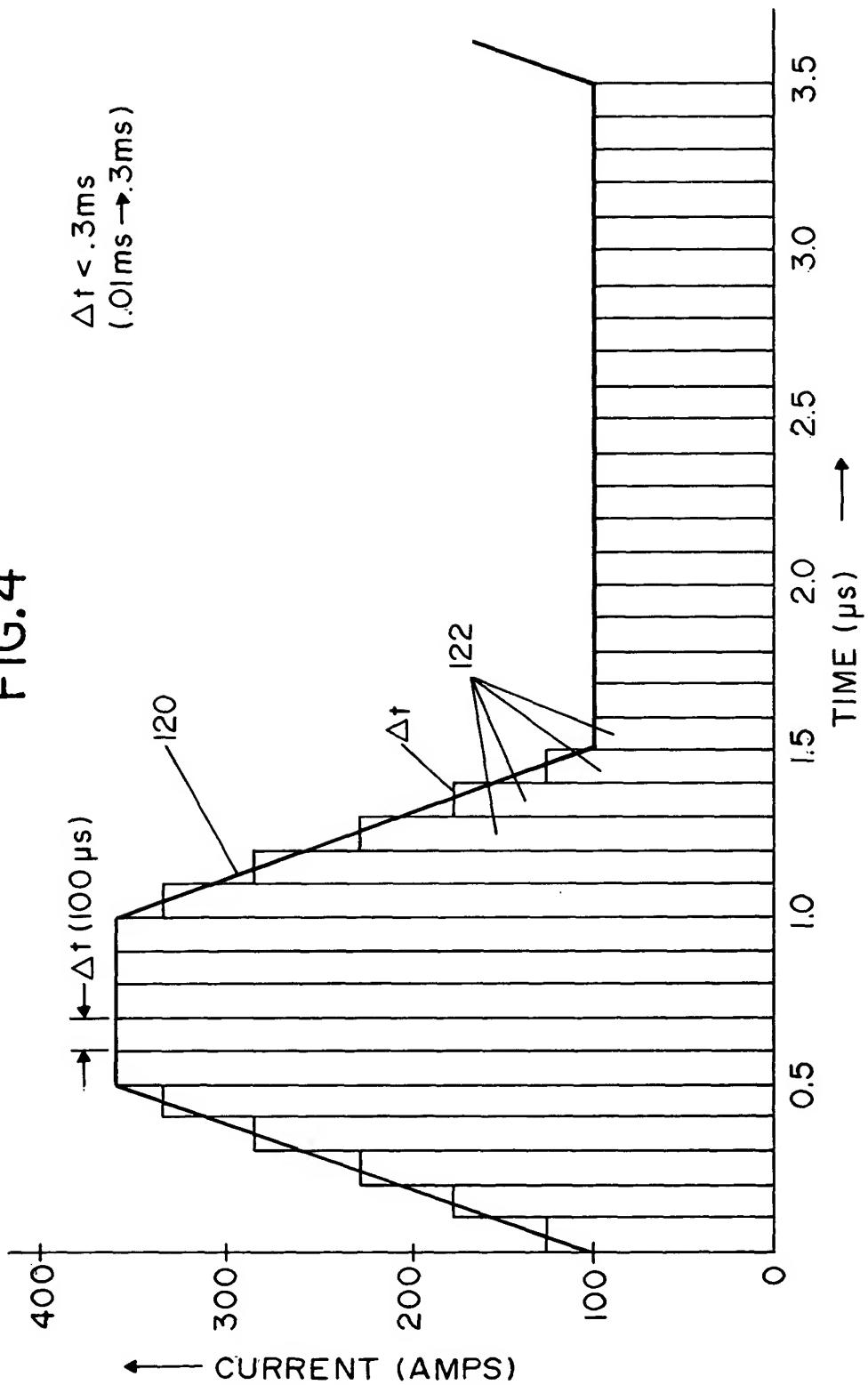


FIG.3



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FIG.4



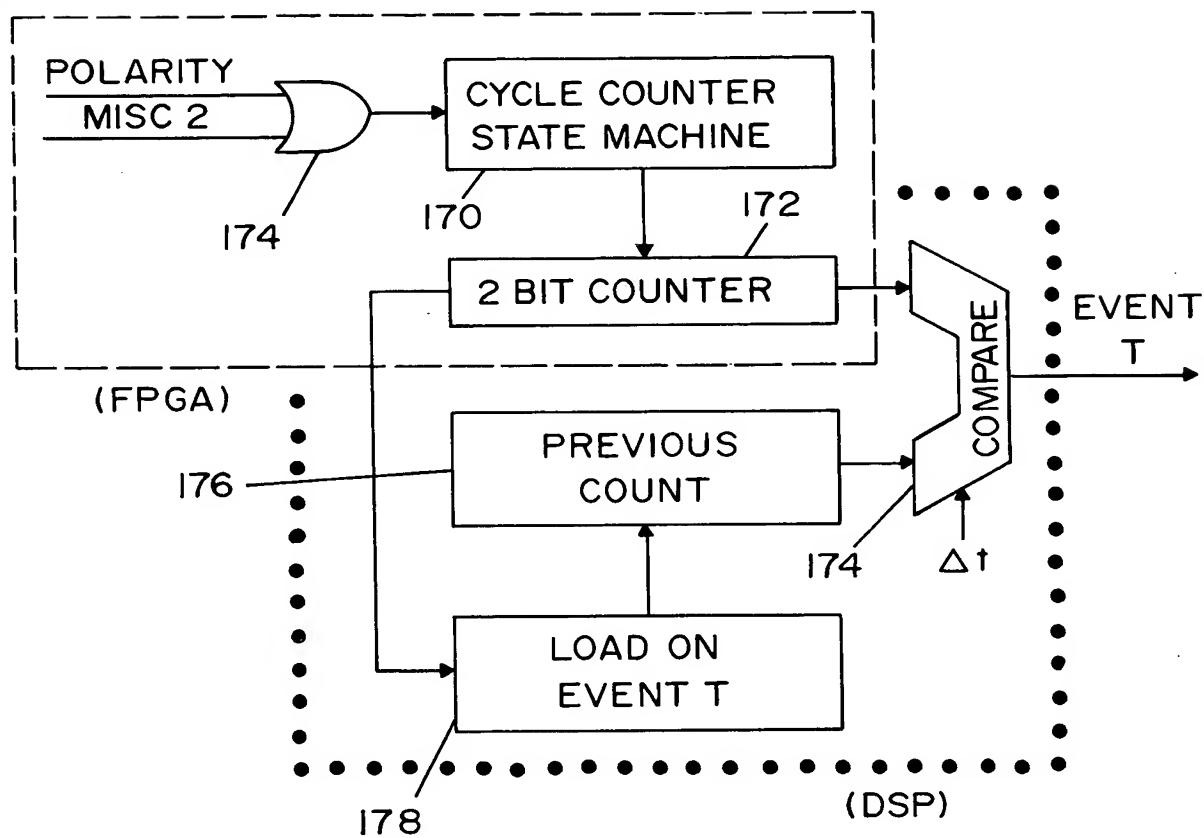


FIG. 5

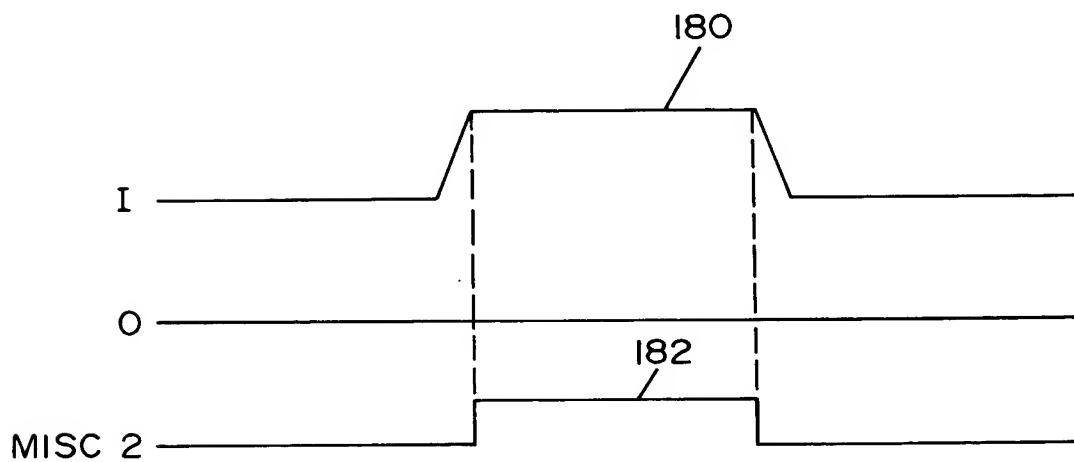
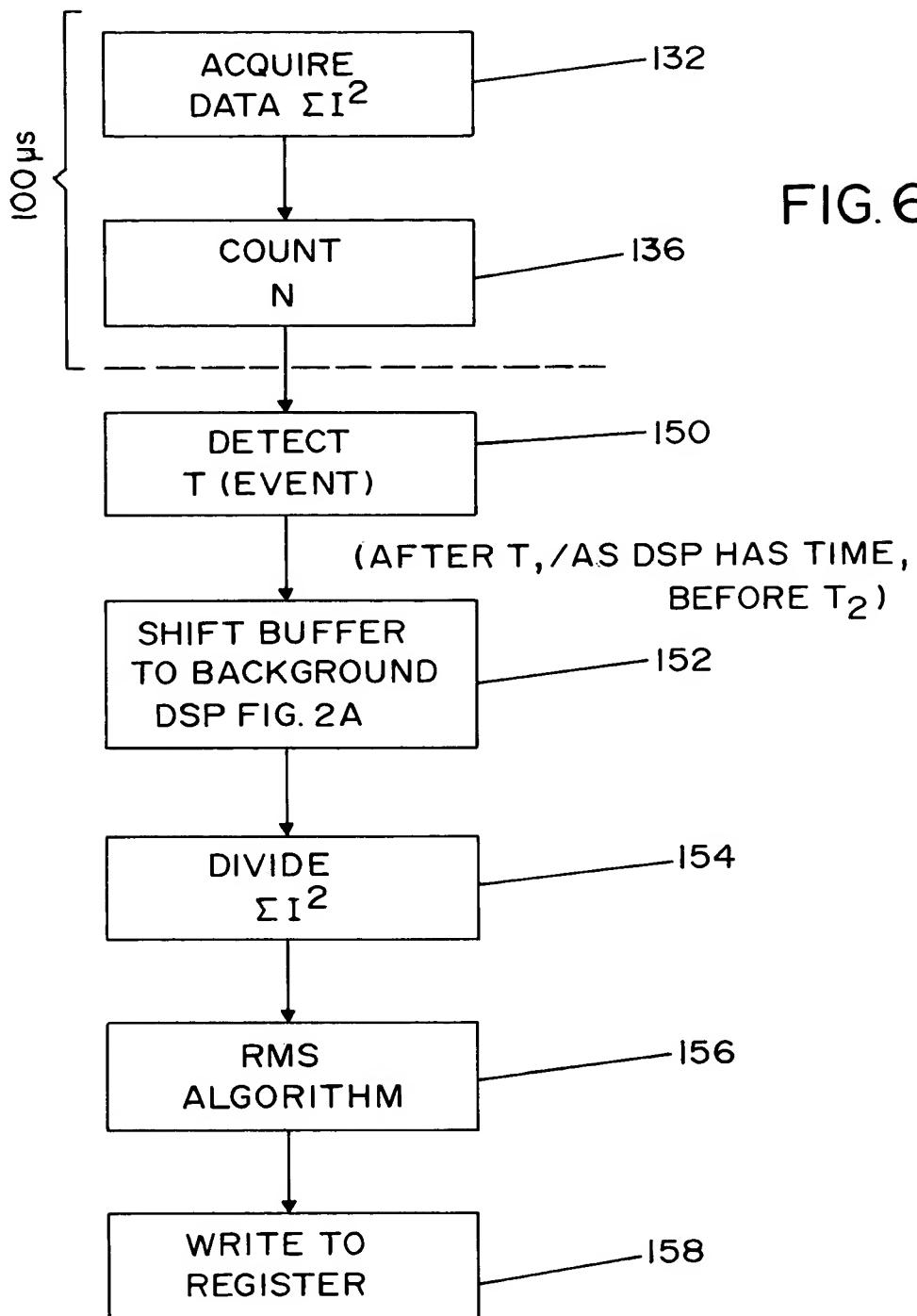
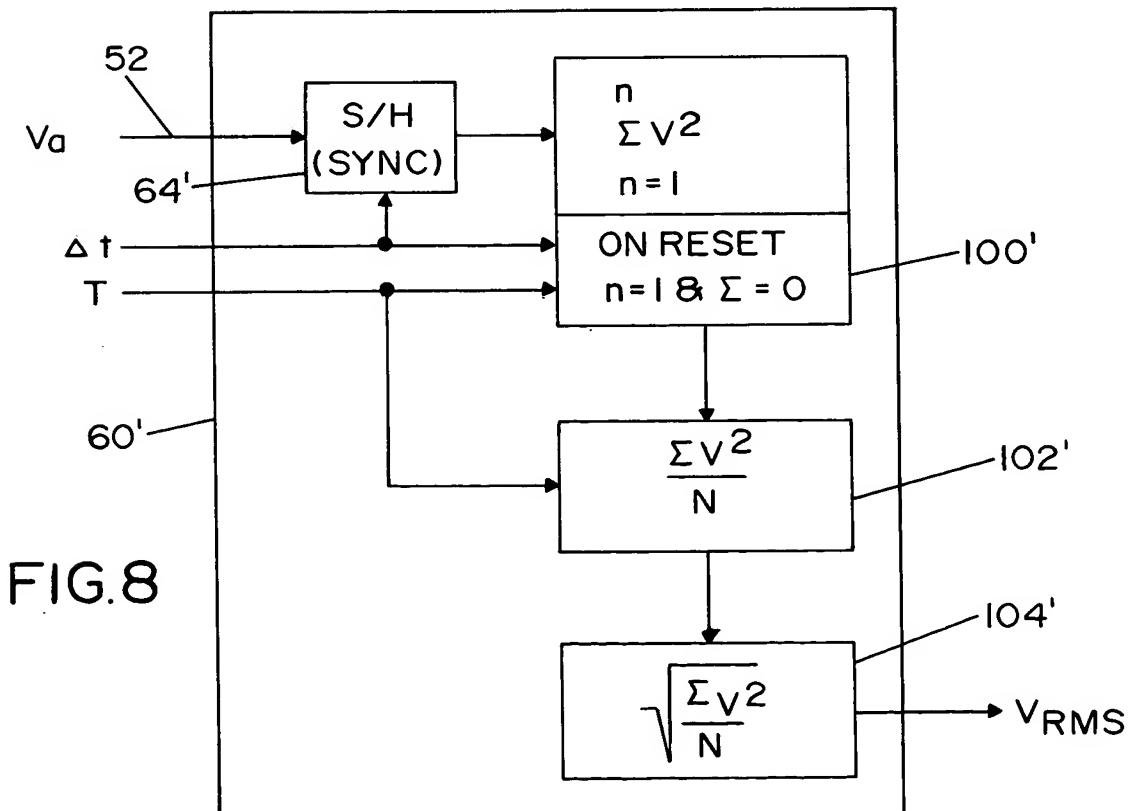
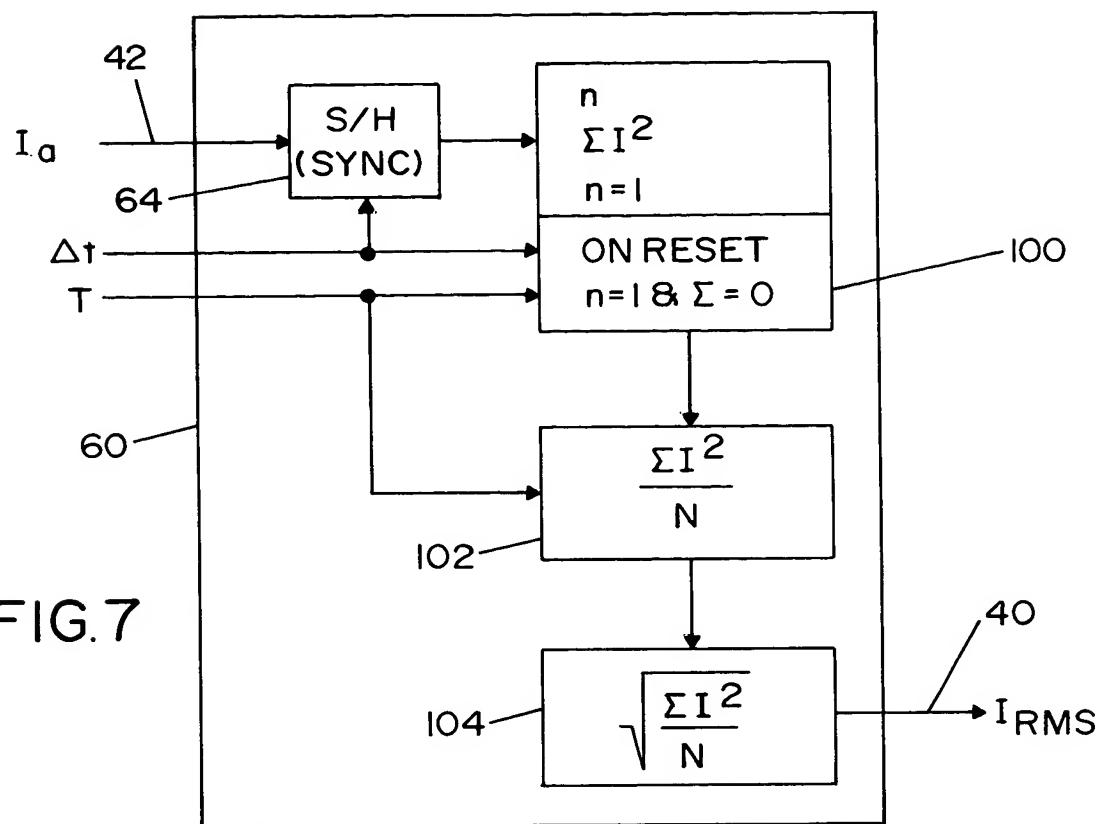


FIG. 5A







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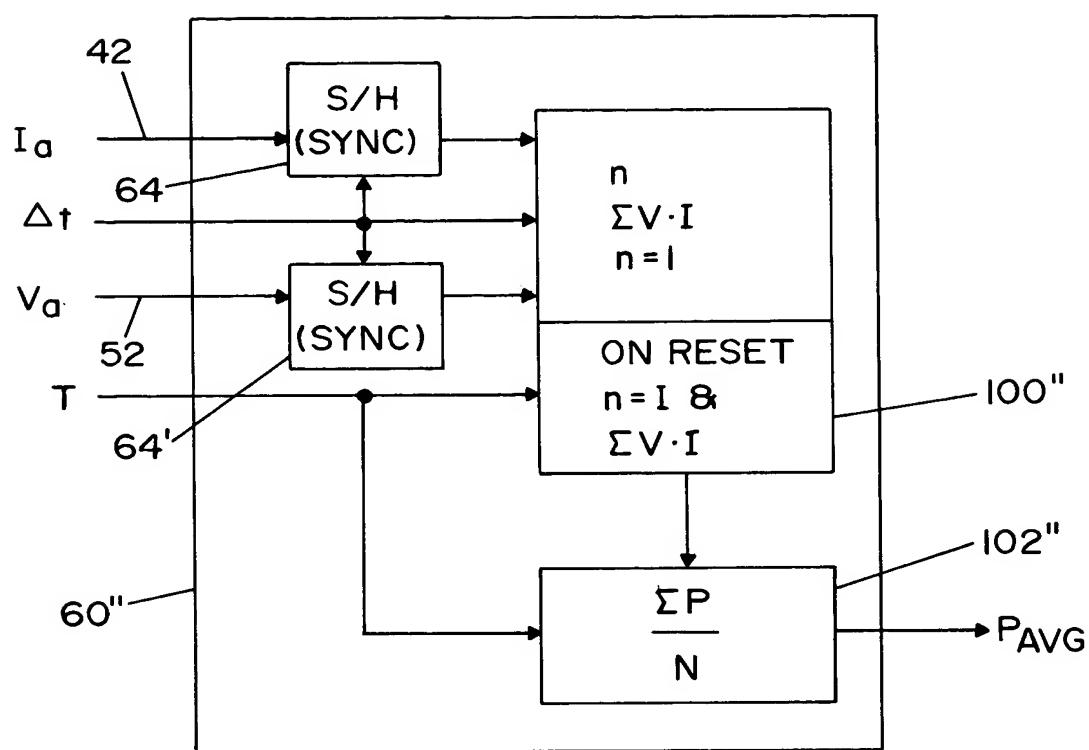


FIG. 9

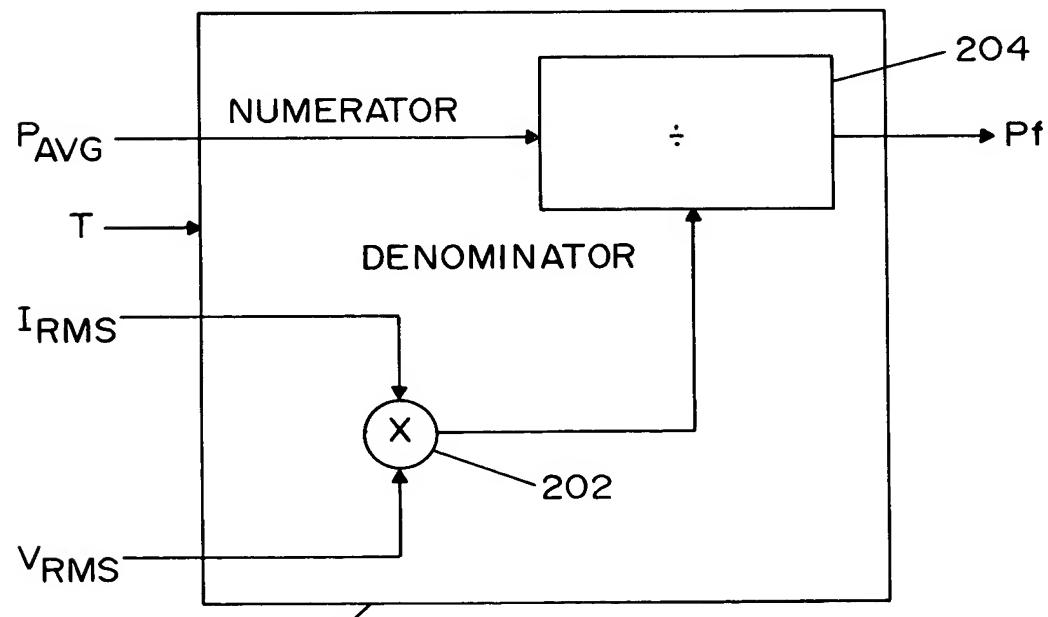


FIG.10



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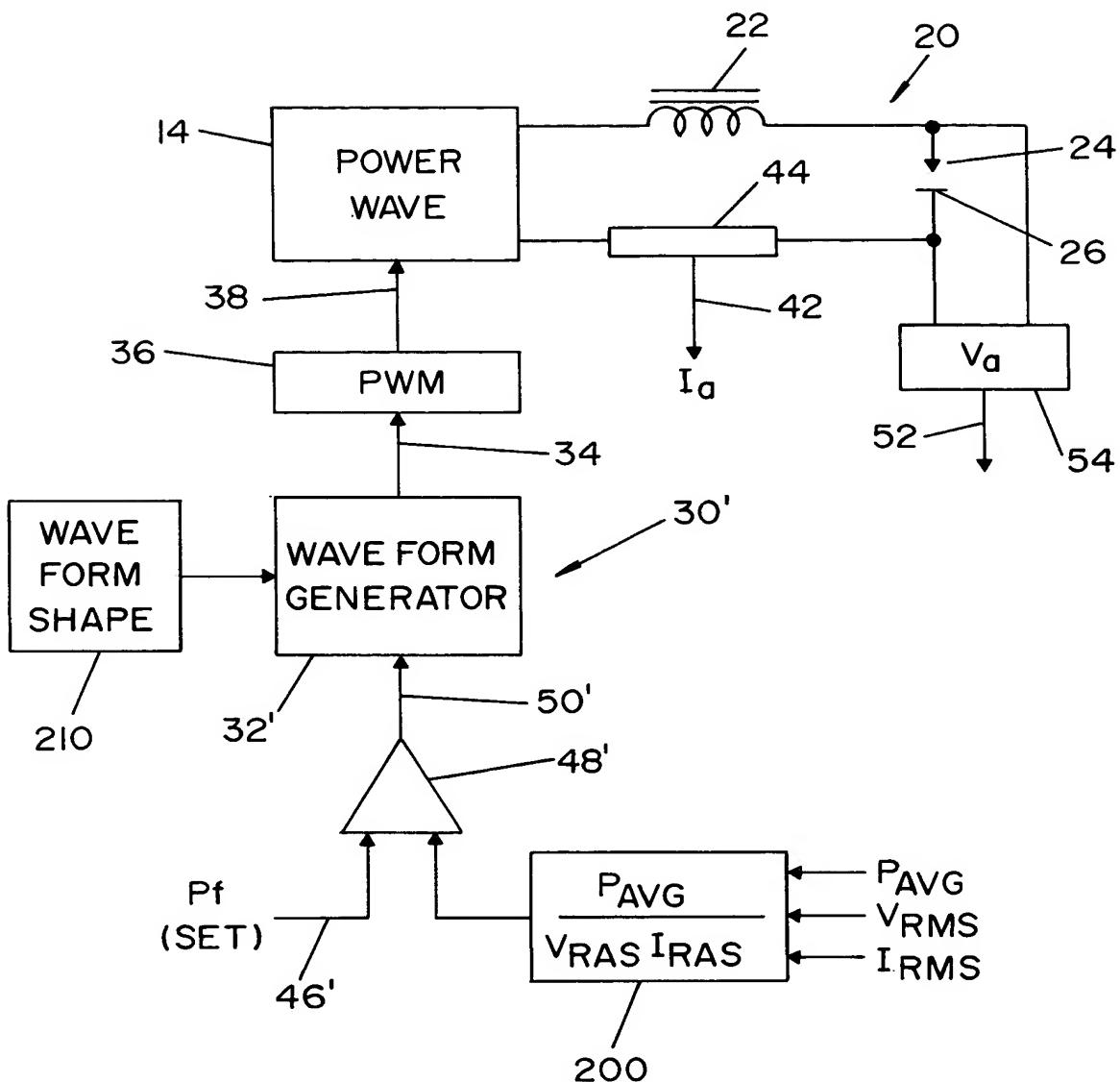


FIG. II



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FIG.12

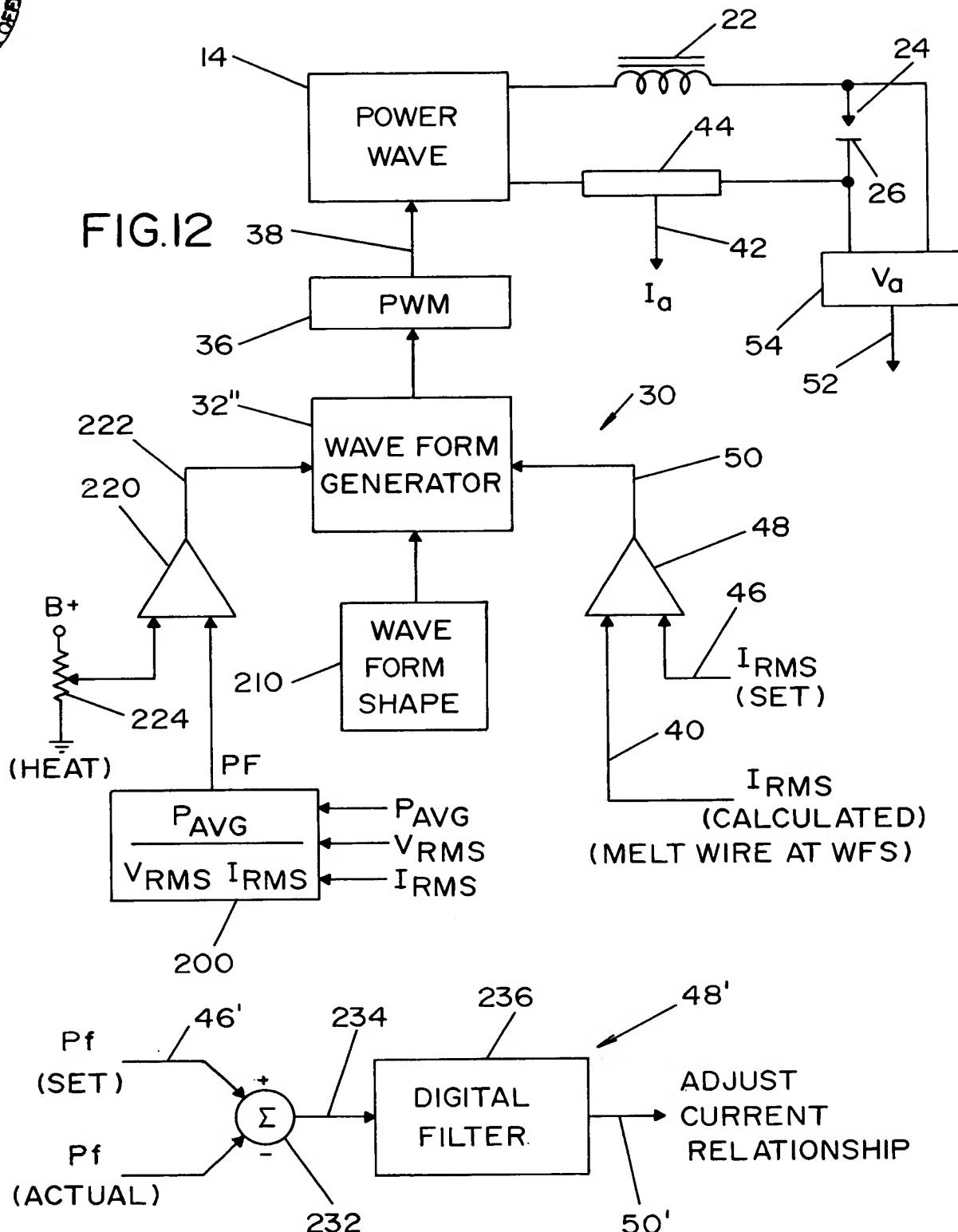


FIG.13



FIG.14

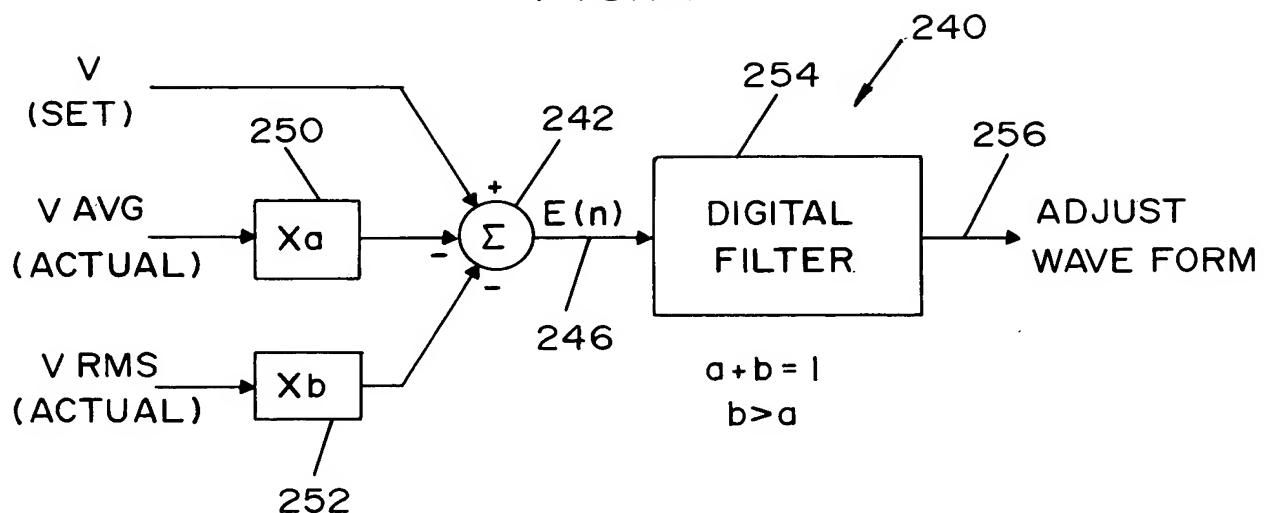


FIG.15

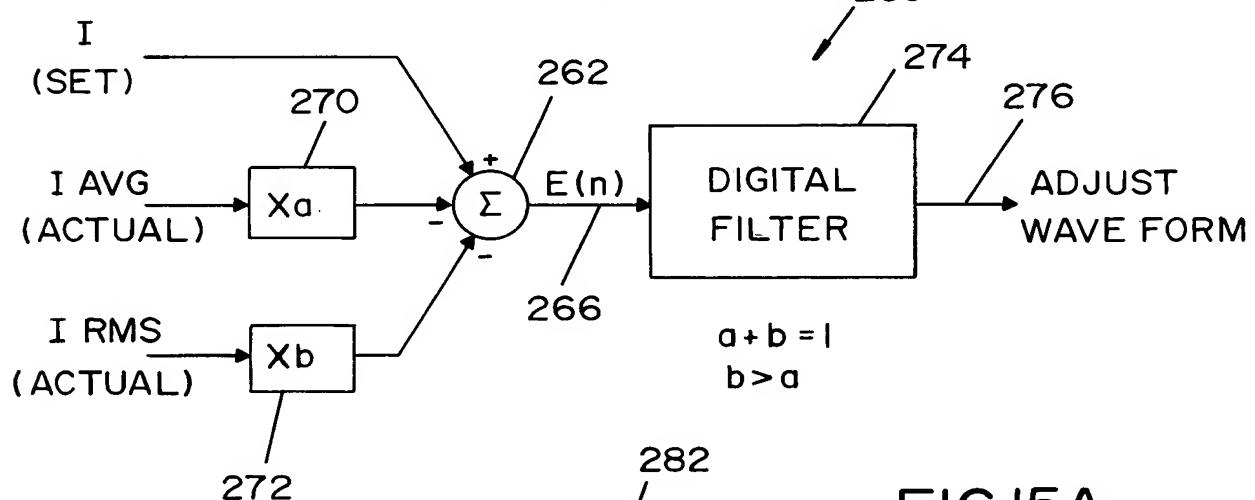
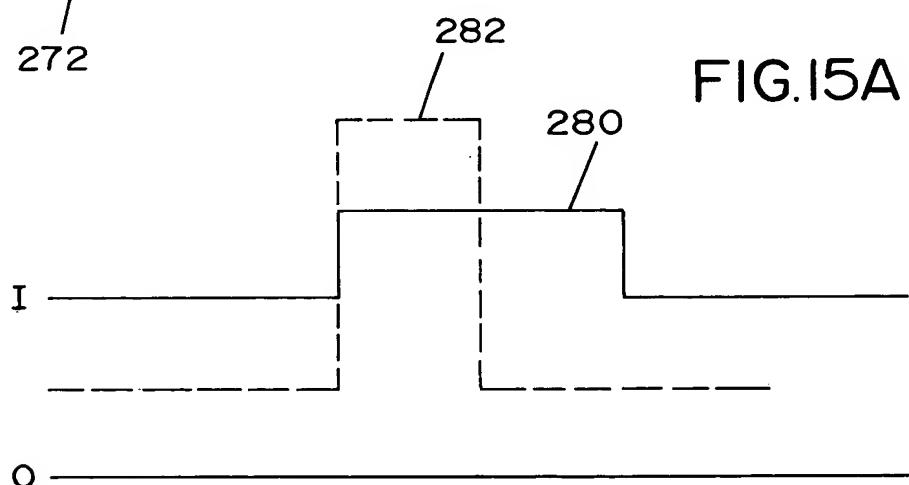


FIG.15A





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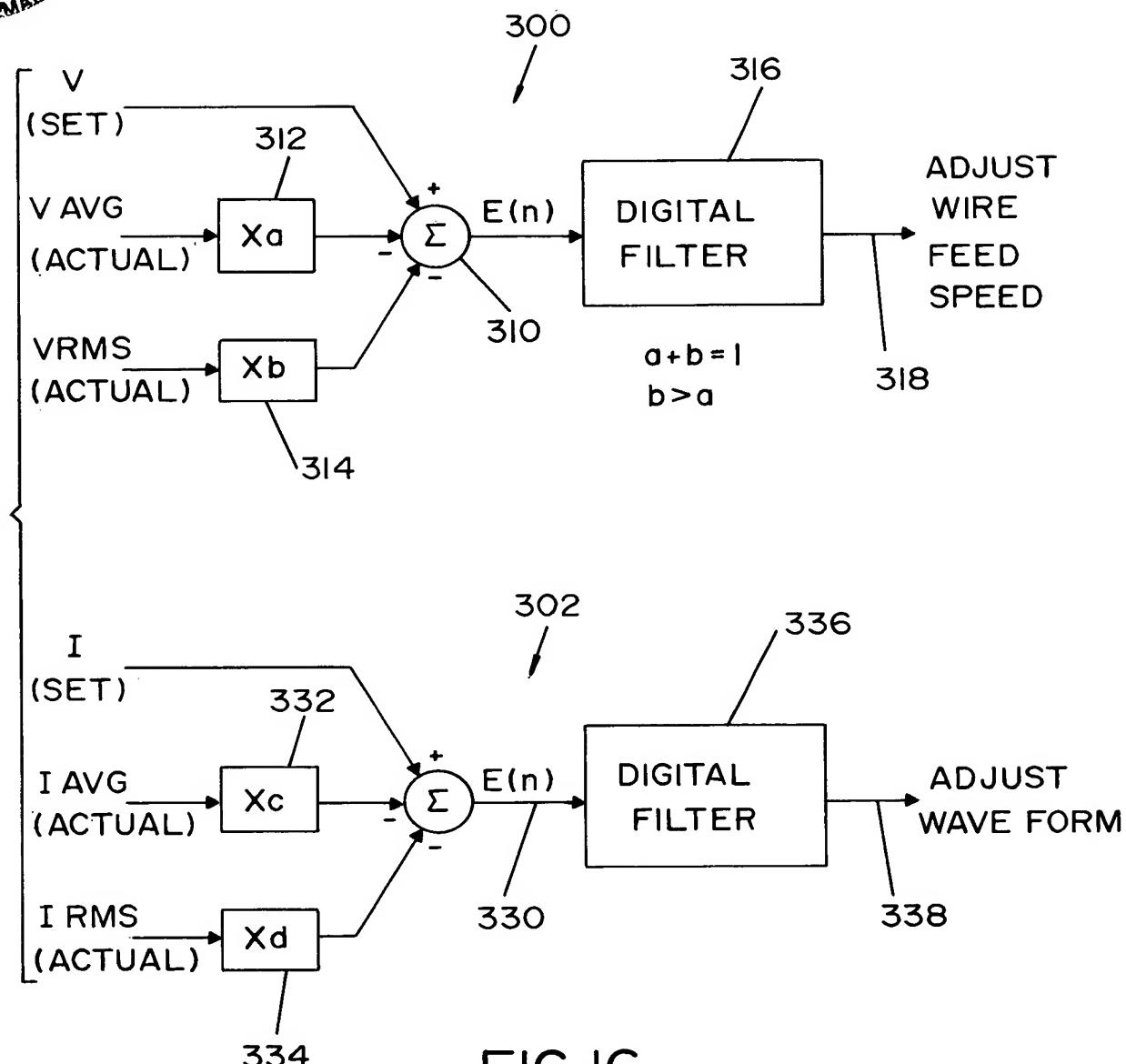


FIG. 16